

Substitute for Form 1449/PTO

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

## Complete if Known


Application Number	10/670,637
Filing Date	09/24/2003
First Named Inventor:	Thomas E. Willis
Art Unit	2188
Examiner Name	Portka, Gary J.
Attorney Docket Number	42P8930XC

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of

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## U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)				
GP 		US-	4,779,188	10-18-1988	Gum et al.	
		US-	4,797,814	01-10-1989	Brenza	
		US-	4,812,969	03-14-1989	Takagi et al.	
		US-	5,317,705	05-31-1994	Gannon et al.	
		US-	5,335,333	08-02-1994	Hinton et al.	
		US-	5,437,016	07-25-1995	Ikegaya et al.	
		US-	5,640,533	06-17-1997	Hays et al.	
		US-	5,940,872	08-17-1999	Hammond et al.	
GP		US-	6,138,225	10-24-2000	Upton et al.	
		US-				
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		US-				
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		US-				

## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>3</sup>
		Country Code <sup>3</sup>	Number <sup>3</sup>	Kind Code <sup>3</sup> (if known)				

Examiner Signature	/Gary Portka/	Date Considered	08/11/2006
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Substitute for Form 1449/PTO <b>CHIEF INFORMATION DISCLOSURE</b> <b>STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)		<b>Complete if Known</b>	
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		Examiner Name	Portka, Gary J.
2 of 2		Attorney Docket Number	42P8930XC
<b>NON PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
GP		Bradley J. Kish et al., "Hobbes: A Multi-Threaded Superscalar Architecture", pgs. 1-20, Department of Electrical Engineering, University of Waterloo, Waterloo, Ontario, Canada.	
GP		Susan J. Eggers et al., "Simultaneous Multithreading: A Platform for Next-Generation Processors", September/October 1997, pgs. 12-19, IEEE Micro.	

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